Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6	(bi-directional or bidirectional or (full adj duplex)) same (system adj clock) same halt$5	USPAT	OR	ON	2005/03/18 10:21
L2	9	emulat$6 and ((halted adj mode) same register)	USPAT	OR	ON	2005/03/18 10:31

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	progrmmable adj clock adj frequency	USPAT	OR	OFF	2005/03/18 17:13
L2	19	programmable adj clock adj frequency	USPAT	OR	OFF	2005/03/18 17:16
L3	0	2 and microcontroller	USPAT	OR	OFF	2005/03/18 17:13
L4	0	2 and (micro adj controller)	USPAT	OR	OFF	2005/03/18 17:13
L5	2	2 and (micro and controller)	USPAT	OR	OFF	2005/03/18 17:13
L6	44	xilinx.as. and clock and dll	USPAT	OR	OFF	2005/03/18 17:16
S3	6	(bi-directional or bidirectional or (full adj duplex)) same (system adj clock) same halt$5	USPAT	OR	ON	2005/03/18 10:21
S4	9	emulat$6 and ((halted adj mode) same register)	USPAT	OR	ON	2005/03/18 12:35
S5	0	vitual adj microcontroller	USPAT	OR	OFF	2005/03/18 12:35
S6	0	virtual adj (micro adj controller)	USPAT	OR	OFF	2005/03/18 12:36
S7	7	virtual adj (microcontroller)	USPAT	OR	OFF	2005/03/18 12:58
S8	2	(("6185522") or ("5964893")).PN.	USPAT	OR	OFF	2005/03/18 12:59
S9	0	S8 and DUT	USPAT	OR	OFF	2005/03/18 14:24
S10	1	("5964893").PN.	USPAT	OR	OFF	2005/03/18 14:24
S11	1	S10 and emulat$7	USPAT	OR	OFF	2005/03/18 17:12